

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants :	Othmar Leitner, et al.	Art Unit :	2822
Serial No. :	10/579,487	Examiner :	Kevin M. Picardat
Filed :	May 16, 2006	Conf. No. :	3015
Title :	METHOD FOR THE PRODUCTION OF TRANSISTOR STRUCTURES WITH LDD		

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT IN REPLY TO OFFICE ACTION OF OCTOBER 2, 2008

In response to the Office Action of October 2, 2008, please amend the above-identified patent application as follows: